



DOCKET NO. R. PULLELA 4-4

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rajasekhar Pullela, *et al.*
Serial No.: 10/033,525
Filed: December 28, 2001
Title: TRANS-ADMITTANCE TRANS-IMPEDANCE
LOGIC FOR INTEGRATED CIRCUITS
Grp./A.U.: 2816
Examiner: Dinh Thanh Le

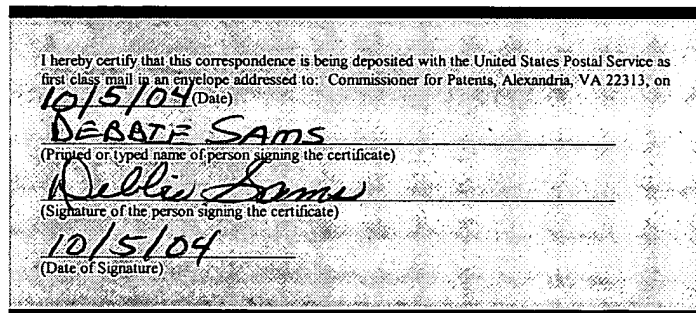
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

10/12/2004 HLE333 00000029 10033525

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180.00 DP



INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
6,140,845	Benachour	October 31, 2000
5,581,215	Ogasawara	December 3, 1996
5,103,117	Voorman et al	April 7, 1992
4,818,952	Kawano et al	April 4, 1989

Reference

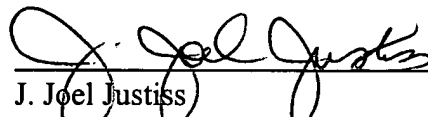
OHHATA et al; "Design of a 32.7-Ghz Bandwidth AGC Amplifier IC with Wide Dynamic Range Implemented in SiGe HBT"; IEEE Journal of Solid-State Circuits, Vol. 34, No. 9, September 1999.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

A check in the amount of \$180.00 is enclosed in payment of the filing fee for this communication. However, the Commissioner is hereby authorized to charge any additional fees connected with this communication or credit any overpayment to Deposit Account No. 08-2395.

Respectfully submitted,

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PTO/SB/08A (08-03)



PTO/SB/08B (08-03)

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/033,525
Filing Date	December 28, 2001
First Named Inventor	Rajasekhar Pullala
Art Unit	2816
Examiner Name	Dinh Thanh Le
Attorney Docket Number	PULLELA 4-4

Sheet	2	of	2
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		OHHATA et al; "Design of a 32.7-Ghz Bandwidth AGC Amplifier IC with Wide Dynamic Range Implemented in SiGe HBT"; IEEE Journal of Solid-State Circuits, Vol. 34, No. 9, September 1999.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.